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Specifications for Type M1 Power Quality Analysers

The main distribution board in a facility installation is supplied via an incomer. This is the facility's first line of measurement for Power Quality and Energy Measurement. Placing a high performance Power Quality meter on the main incomer to a facility facilitates measurement of the quality of power coming into the facility and also verification of electricity bills. This helps in doing a monthly comparison between the consumption as measured and the consumption reported on the bill.

When choosing a Power Quality Analyser, check how the vendor's product specifications compare with this suggested set of minimum requirements for a Power Quality Analyser.

Typical use might be:

- Main incomers or on Sub-mains to sensitive equipment, e.g. medical centres, data centres, training simulators, laboratories etc.

All PQ Analysers shall meet the following minimum requirements:

- Operating temperature range of -25 C to +70C
- Class 0.2S
- Circuit boards to be tropicalized
- Enclosure rating of IP52 or better
- Industrial grade components
- 3 years Manufacturer's Warranty as a minimum
- Australia "C" Tick approval
- Backlit 5.7" colour LCD display
- Screen – min 640 x 480 resolution
- Metal case
- Panel mount 144mm x 144mm
- IEC 62053-22 Class 0.2S accuracy for KWH (1% to 120% of In)
- Starting current less than 0.1% of In
- 1GB or more of on board memory
- Neutral current (I_N), Ground current
- V4 input
- VLN and VLL per phase and average
- Current per phase and average
- Frequency
- KW per phase and total
- KVAR per phase and total
- KVA total
- PF per phase and total
- KVAH
- KWH import/export
- KVARH import/export
- Demands and TOU
- Wave form recording of 128 samples per cycle for 10 cycles for V and I
- 63rd individual harmonic on board and 256th by software
- Harmonic power and energy to 63rd
- SOE and min/max log
- PQ log for Sags and Swells, Transients and Flicker
- 1 x Ethernet (100 baseT or 100 baseFX) and 2 x optically isolated RS485 ports
- Modbus RTU, Modbus TCP and Ethernet Gateway protocols
- Battery backed real time clock
- V and I unbalance
- 8 DI (self excited) and 4 DO, 4 relay output
- Optional IRIG-B input
- CETA PMC-670 or equivalent